

# Mohsen Ghorbanali Afjeh



## CONTACT

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## CAREER & AFFILIATE

Senior Researcher in Power  
Electronics.

Converters & Power  
Supplies research team on  
JDEVS - Iranian Research  
Institute for Electrical  
Engineering

## SOFTWARE

Matlab, Vivado, Code  
Composer Studio , Visual  
Studio

## Born

september 16, 1984,  
Tehran, Iran

## Profile

Electrical Engineer with 7+ years of experience working with JDVES in the field of power electrical engineering. Primarily focused on implementation of digital control systems.

## Education

### Ph. D. Candidate

2017- Present

Power Electrical Engineering, School of Electrical Engineering, Islamic Azad university, Tehran, Iran

Advisors: Dr. Majid Babaei

### M. Sc.

2008-2010

Power Electrical Engineering- Electric Energy Management , School of Electrical Engineering, Amirkabir University of Technology - Tehran Polytechnic, Iran

Advisors: Dr. Seyed Hamid Fathi

Thesis: The location of FACTS devices in the restructured power system

## Experience

### Power Electronics technologist JDEVS

2013-2017

Vice Presidency of Science and Technology, National Plan: "Localization of the Metro Driving System".